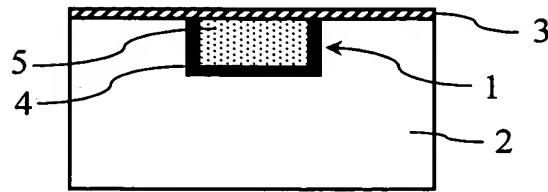
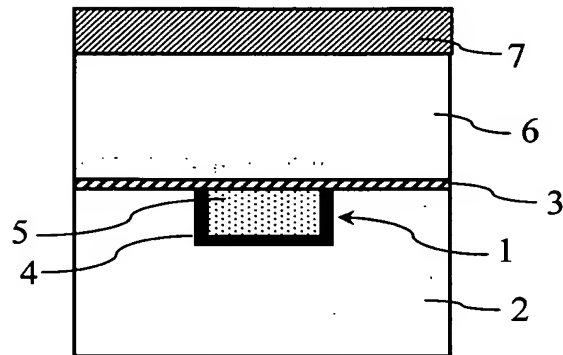


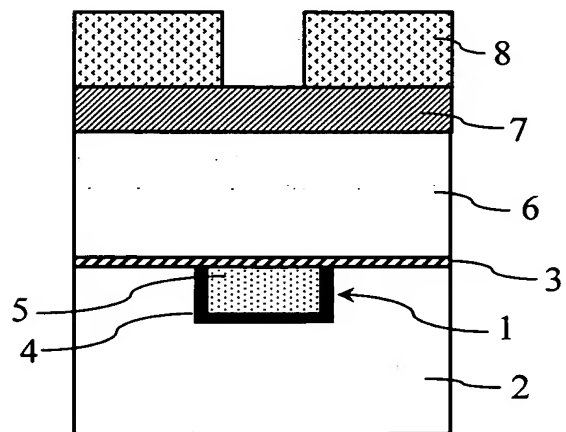
*Fig.1A*



*Fig.1B*



*Fig.1C*

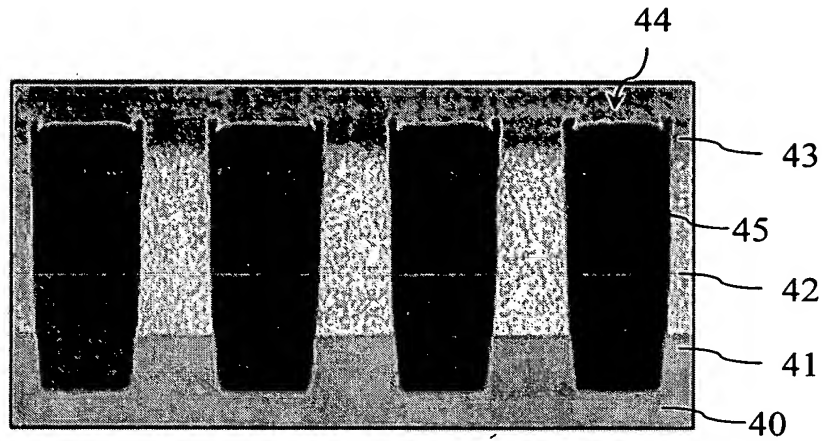


A cross-sectional view of a semiconductor device. A substrate 2 is shown at the bottom. A layer 1 is formed on the substrate. A horizontal layer 3 is formed on layer 1. A central region 3a is defined within layer 3. Two vertical pillars 6 are formed on the substrate, each containing a core 7 and an outer layer 8. The pillars are separated by a gap 9. A layer 5 is formed on the substrate, and a layer 4 is formed on layer 5. An arrow points to the central region 3a.

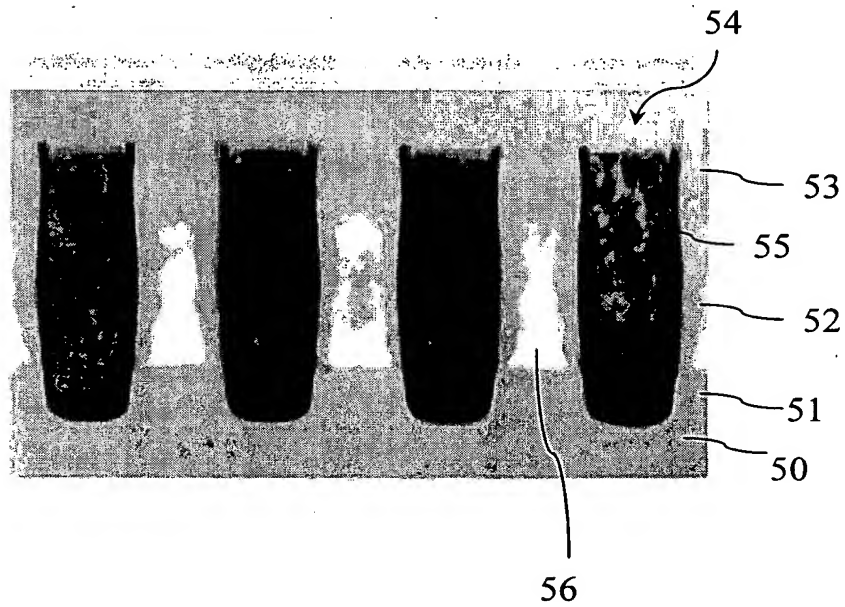
A cross-sectional view of a semiconductor device. It features a central opening 10. The opening is flanked by two vertical structures, each containing a hatched top layer 7 and a white middle layer 6. Below these is a horizontal layer 3. At the bottom of the opening is a rectangular region 1 filled with a stippled pattern. This region is surrounded by a thick black border 4. The entire structure is supported by a base layer 2. A layer 5 is shown on the left side of the central region.

A cross-sectional view of a semiconductor device. A central T-shaped structure (1) is formed in a substrate (2). The T-shaped structure has a top layer (7) and a bottom layer (4). The top layer (7) is a solid layer, and the bottom layer (4) is a dotted layer. The T-shaped structure is surrounded by a layer (6). A horizontal layer (10) is located below the T-shaped structure. A layer (3) is located below the horizontal layer (10). A layer (5) is located below the T-shaped structure. A layer (1) is located below the T-shaped structure. A layer (2) is located below the T-shaped structure. A layer (11) is located above the T-shaped structure. A layer (12) is located above the T-shaped structure. A layer (13) is located above the T-shaped structure. A layer (14) is located above the T-shaped structure. A layer (15) is located above the T-shaped structure.

*Fig. 2A*

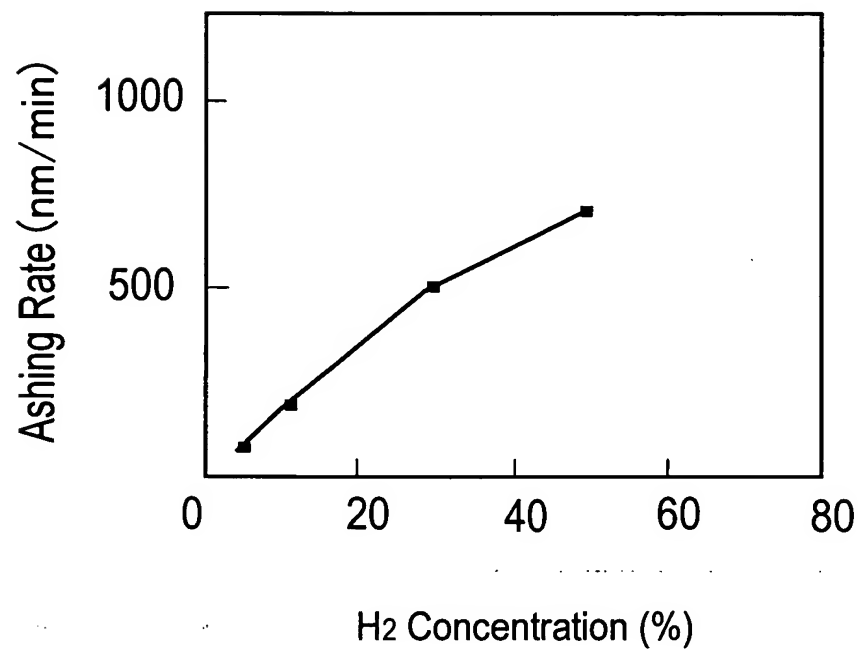


*Fig. 2B*

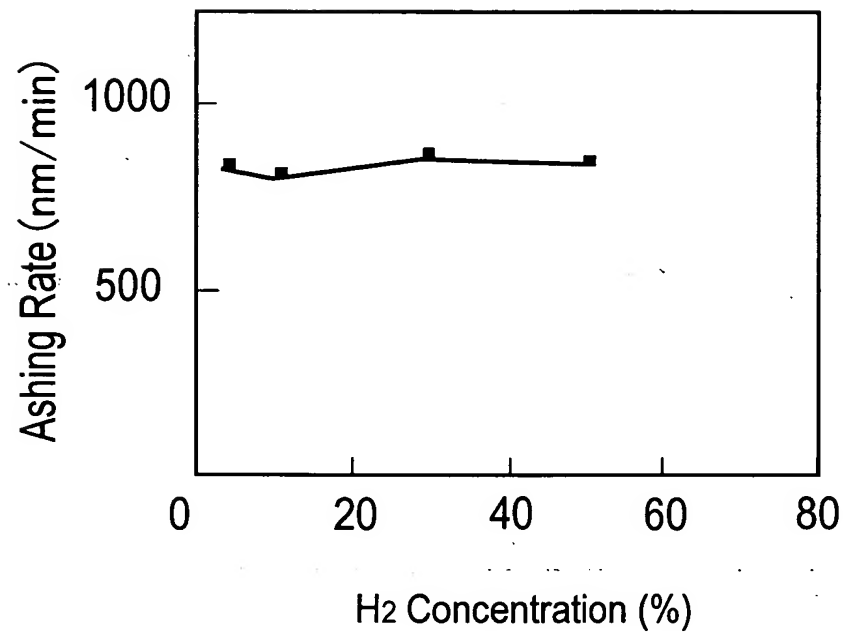


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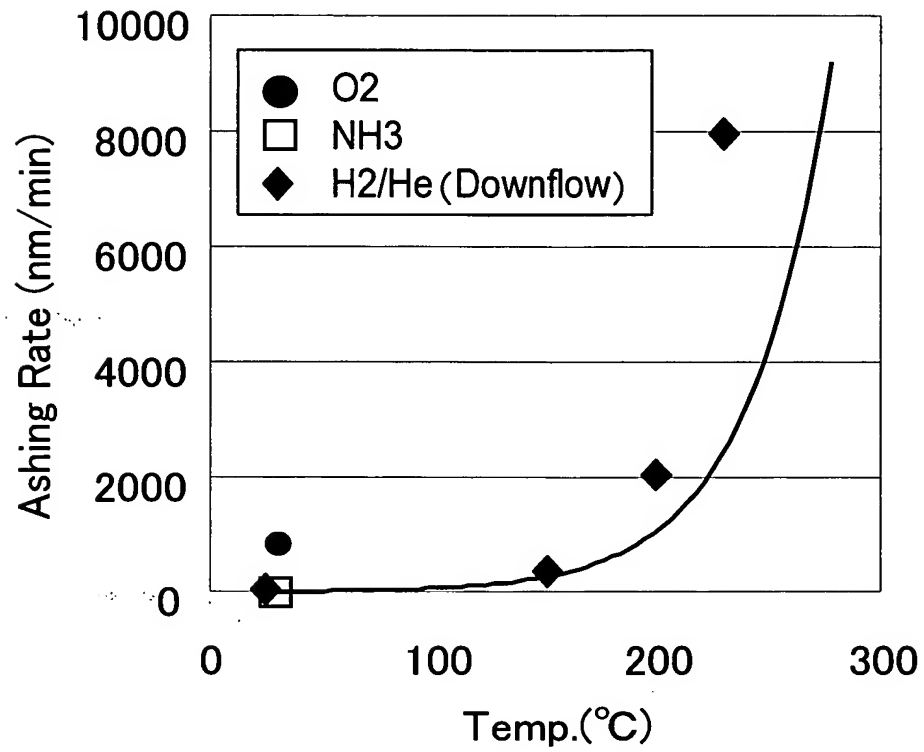
*Fig.3*



*Fig. 4*

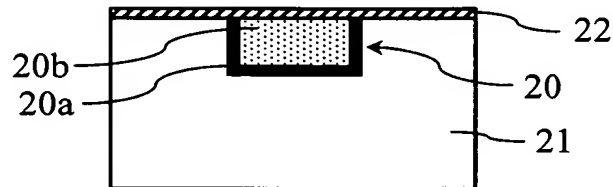


*Fig.5*



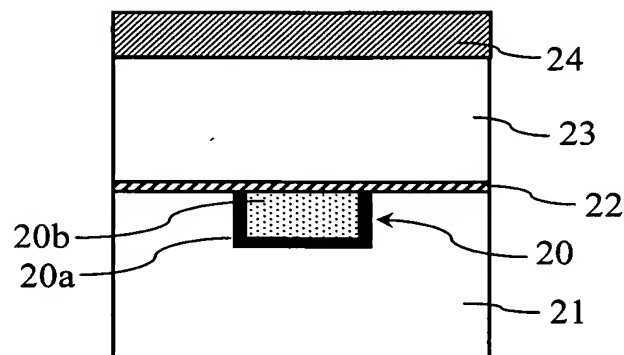
*Fig. 6A*

*Prior Art*



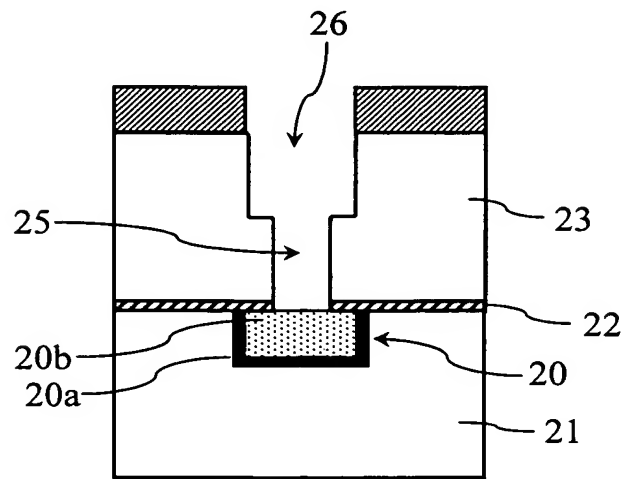
*Fig. 6B*

*Prior Art*



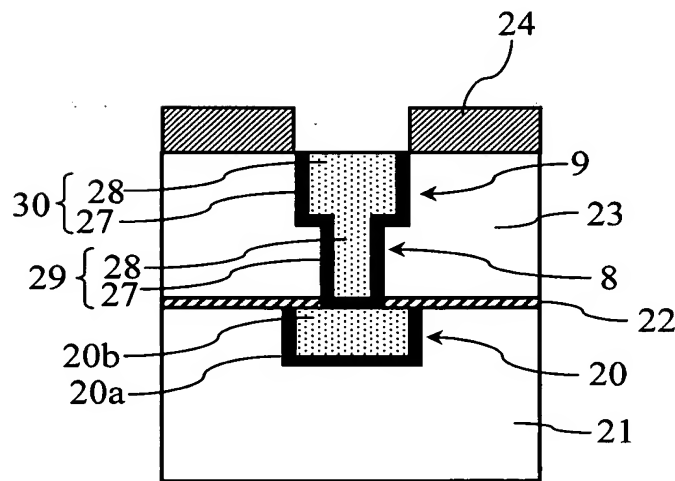
*Fig. 6C*

*Prior Art*



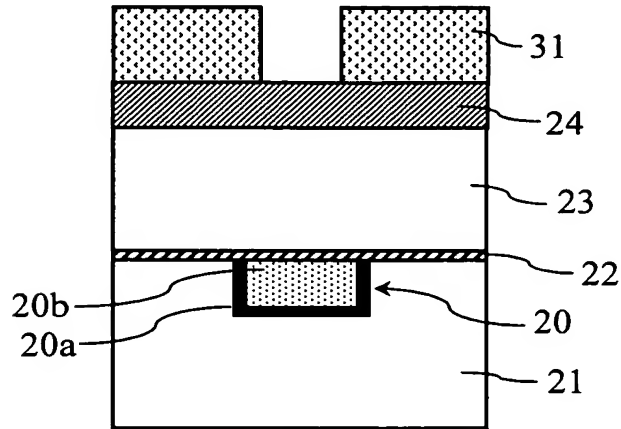
*Fig. 6D*

*Prior Art*



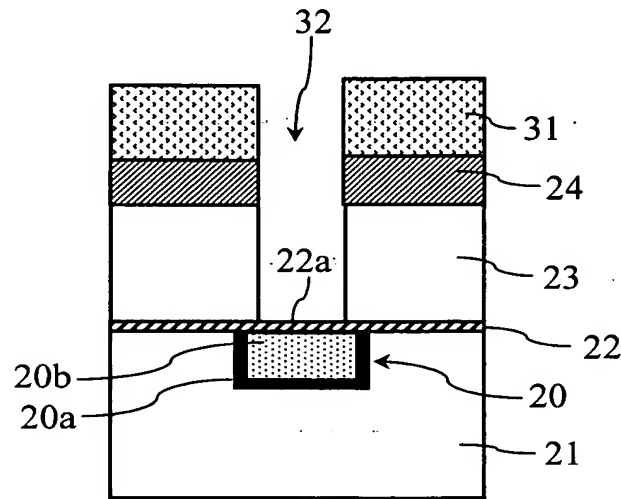


*Fig. 7A*



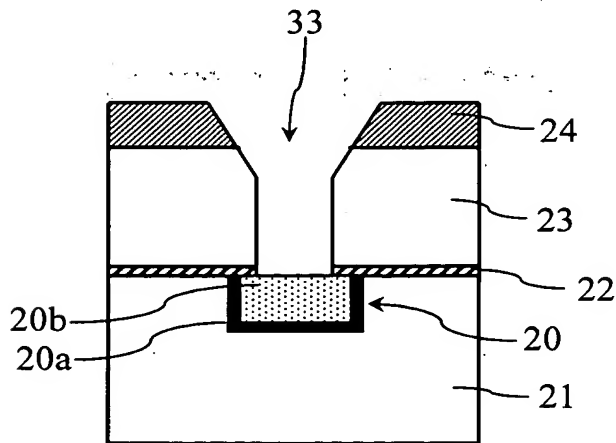
*Prior Art*

*Fig. 7B*



*Prior Art*

*Fig. 7C*



*Prior Art*